

CMOS For JBSN1

Use these Settings with AMI BIOS version 2.01

Sigma Board

FULL WIDTH LONG, 8 STICKS RAM

STICKING UP.

Standard CMOS Setup

SOLDERED BATTERY

PN: BT 111

Set Date
Set Time
Daylight Saving: Disabled
Hard Disk C:

Set according to settings on Drive or see page 6.

Type=47, Cyln=###, Head=##, Wpcom=0, Lzone=###, Sect=##, Size=???MB
(Type always =47, Wpcom always=0, Lzone always=Cyln, Size is set by CMOS)

Hard Disk D: Not Installed
Floppy Drive A: 1.44MB 3 1/2"
Floppy Drive B: Not Installed
Primary Display: VGA/PGA/EGA
Keyboard: Not Installed

Advanced CMOS Setup

Typematic Rate Programming: Enabled
Typematic Rate Delay (msec): 500
Typematic Rate (chars/sec): 15
Above 1 MB Memory Test: Disabled
Memory Test Tick Sound: Disabled
Memory Parity Error Check: Disabled
Hit Message Display: Enabled
Hard Disk Type 47 RAM Area: 0:300
Wait for <F1> If Any Error: Enabled
System Boot Up Num Lock: Off
Weitek Processor: Absent
Floppy Drive Seek at Boot: Disabled
**System Boot Up Sequence: A:, C: (install)
C:, A: (normal)**
System Boot Up CPU Speed: High
Cache Memory: Both
Gate A20 Emulation: Both
Turbo Switch Function: Disabled
Password Checking Option: Disabled
Video ROM Shadow C000, 16K: Enabled
Video ROM Shadow C400, 16K: Enabled
Adaptor ROM Shadow C800, 16K: Disabled
Adaptor ROM Shadow CC00, 16K: Disabled
Adaptor ROM Shadow D000, 16K: Disabled
Adaptor ROM Shadow D400, 16K: Disabled
Adaptor ROM Shadow D800, 16K: Disabled
Adaptor ROM Shadow DC00, 16K: Disabled
Adaptor ROM Shadow E000, 64K: Disabled
System ROM Shadow F000, 64K: Enabled
Boot Sector Virus Protection: Disabled

Advanced CHIPSET Setup

Low Speed CPU Clock Select: CLKIN
I/O Recovery Time Delay: 2 BCLK
Memory Remapping: Enabled
E0000 ROM Belongs to ATBUS: No
Memory above 16MB Cacheable: Yes
C0000-C3FFF, 16K Cacheable: Yes
C4000-C7FFF, 16K Cacheable: Yes
C8000-CBFFF, 16K Cacheable: No
CC000-CFFFF, 16K Cacheable: No
D0000-D3FFF, 16K Cacheable: No
D4000-D7FFF, 16K Cacheable: No
D8000-DBFFF, 16K Cacheable: No
DC000-DFFFF, 16K Cacheable: No
E0000-EFFFF, 64K Cacheable: No
F0000-FFFFFF, 64K Cacheable: Yes
Non-Cacheable Block-1 Enable: Disabled
Non-Cacheable Block-1 Size: 1MB
Non-Cacheable Block-1 Base: 0KB
Non-Cacheable Block-2 Enable: Disabled
Non-Cacheable Block-2 Size: 16MB
Non-Cacheable Block-2 Base: 0KB
Co-Processor Ready# Delay: Enabled
Check ELBA# Signal: T1
RAS Time Out: Disabled
Force ALT Bit Active: No
Local Bus Ready Delay: Disabled
DMA Clock Select: SCLK/2

Use these Settings with AMI BIOS version 1.00 or 1.30 Sigma Board

Standard CMOS Setup

Set Date
 Set Time
 Daylight Saving: Disabled
 Hard Disk C:

BATTERY PN: B7111

Set according to settings on Drive or see page 6.

Type=47, Cyl=###, Head=##, Wpcom=0, Lzone=###, Sect=##, Size=???MB
 (Type always =47, Wpcom always=0, Lzone always=Cyl, Size is set by CMOS)

Hard Disk D: Not Installed
 Floppy Drive A: 1.44MB 3 1/2"
 Floppy Drive B: Not Installed
 Primary Display: VGA/PGA/EGA
 Keyboard: Not Installed

Advanced CMOS Setup

Typematic Rate Programming: Enabled
 Typematic Rate Delay (msec): 500
 Typematic Rate (chars/sec): 15
 Above 1 MB Memory Test: Disabled
 Memory Test Tick Sound: Disabled
 Memory Parity Error Check: Disabled
 Hit Message Display: Enabled
 Hard Disk Type 47 RAM Area: 0:300
 Wait for <F1> If Any Error: Enabled
 System Boot Up Num Lock: Off
 Weitek Processor: Absent
 Floppy Drive Seek at Boot: Disabled
**System Boot Up Sequence: A:, C: (install)
 C:, A: (normal)**
 System Boot Up CPU Speed: High
 Cache Memory: Both
 Gate A20 Emulation: Both
 Password Checking Option: Disabled
 Video ROM Shadow C000, 16K: Enabled
 Video ROM Shadow C400, 16K: Enabled
 Adaptor ROM Shadow C800, 16K: Disabled
 Adaptor ROM Shadow CC00, 16K: Disabled
 Adaptor ROM Shadow D000, 16K: Disabled
 Adaptor ROM Shadow D400, 16K: Disabled
 Adaptor ROM Shadow D800, 16K: Disabled
 Adaptor ROM Shadow DC00, 16K: Disabled
 Adaptor ROM Shadow E000, 64K: Disabled
 System ROM Shadow F000, 64K: Enabled

Advanced CHIPSET Setup

Auto-Configuration: Disabled
 CPU Low Speed Clock: CLKIN
 XA Bus Delay: Disabled
 AT Bus Clock: CPUCLK/4
 Keyboard Clock Selection: 7.2Mhz
 I/O Recovery Time Delay: 2 BCLK
 Fast Cache Read Hit: Disabled
 Fast Cache Write Hit: Disabled
 Fast Page Mode DRAM: Disabled
 DRAM Wait States: 2
 Memory Remapping: Enabled
 Memory above 16MB Cacheable: Yes
 C0000-C3FFF, 16K Cacheable: Yes
 C4000-C7FFF, 16K Cacheable: Yes
 C8000-CBFFF, 16K Cacheable: No
 CC000-CFFFF, 16K Cacheable: No
 D0000-D3FFF, 16K Cacheable: No
 D4000-D7FFF, 16K Cacheable: No
 D8000-DBFFF, 16K Cacheable: No
 DC000-DFFFF, 16K Cacheable: No
 E0000-EFFFF, 64K Cacheable: No
 F0000-FFFFFF, 64K Cacheable: Yes
 Non-Cacheable Block-1 Enable: Disabled
 Non-Cacheable Block-1 Size: 1MB
 Non-Cacheable Block-1 Base: 0KB
 Non-Cacheable Block-2 Enable: Disabled
 Non-Cacheable Block-2 Size: 16MB
 Non-Cacheable Block-2 Base: 0KB
 Co-Processor Ready# Delay: Enabled
 Check ELBA# Pin: T1
 DMA CAS Timing Delay: Disabled
 DMA Clock Select: SCLK/2

Use these Settings with AMI BIOS version 2.

Teknor Board - 2 STICKS RAM, FLAT,

3/4
OF
W/STH
LOWE.

Standard CMOS Setup

Set Date
Set Time
Daylight Saving: Disabled
Hard Disk C:

BATTERY PN: BT114

Set according to settings on Drive or see page 6.

Type=47, Cyln=###, Head=##, Wpcom=0, Lzone=###, Sect=##, Size=???MB
(Type always =47, Wpcom always=0, Lzone always=Cyln, Size is set by CMOS)

Hard Disk D: Not Installed
Floppy Drive A: 1.44MB 3 1/2"
Floppy Drive B: Not Installed
Primary Display: VGA/PGA/EGA
Keyboard: Not Installed

Advanced CMOS Setup

Typematic Rate Programming: Enabled
Typematic Rate Delay (msec): 500
Typematic Rate (chars/sec): 15
Above 1 MB Memory Test: Disabled
Memory Test Tick Sound: Disabled
Hit Message Display: Enabled
Hard Disk Type 47 RAM Area: 0:300
System Boot Up Num Lock: Off
Floppy Drive Seek at Boot: Disabled
**System Boot Up Sequence: A:, C: (install)
C:, A: (normal)**
System Boot Up CPU Speed: High
Internal Cache Memory: Both
Gate A20 Emulation: Both
Passwork Checking Option: Setup
Adaptor ROM Shadow C800, 16K: Disabled
Adaptor ROM Shadow CC00, 16K: Disabled
Adaptor ROM Shadow D000, 16K: Disabled
Adaptor ROM Shadow D400, 16K: Disabled
Adaptor ROM Shadow D800, 16K: Disabled
Adaptor ROM Shadow DC00, 16K: Disabled
Adaptor ROM Shadow E000, 64K: Disabled
Adaptor ROM Shadow E400, 64K: Disabled
Adaptor ROM Shadow E800, 64K: Disabled
Adaptor ROM Shadow EC00, 64K: Disabled
System RAM Option: Both

Advanced CHIPSET Setup

Read Cycle Start Point: Mid T2
RAS Precharge Time: 2 CLK2s
RAS to CAS Delay: 2 CLK2s
CAS Width for Read/Write: 1.5/1
RAS Time Out: Disabled
MA[10:0] & RAMW# Drive (mA): 24-24-24
Refresh Mode: Sync
Refresh Type: RAS ONLY
Refresh Period: 15.625
Page Mode: Enabled
Ext. Parity Error Input: Disabled
Parity Generation: Enabled
Middle Bios: Disabled
ROM Wait States: 1 W/S
Slow CPU Speed Emulation: /4
Slow CPU Clock Divider: /2
Fast BUS CLK Freq. Change: Disabled
Fast BUS CLOCK Region Addr: 0 KB
Fast BUS CLK Divider: /2
Slow BUS CLK Divider: /2
Extended DMA Mode: Disabled
Extended DMA Page: Lower
8-Bit DMA Wait States: 3 W/S
16-Bit DMA Wait States: 3 W/S
DMA Clock: SCLK/2
DMA MEMR# Signal Delay: Normal
I/O Address Decode: 16 Bit
Slot Drive Current: 24 mA
Disk I/O Timing: Fast
Command Delay - I/O Cycle: 1
Command Delay - Mem Cycle: 1
16-Bit Wait States: 1 W/S
8-Bit Wait States: 5 W/S
System Clock in Sleep Mode: Disabled
Fast Internal RC#: Enabled
Conditional Wait States: Enabled

Obsession CMOS Settings for Hard Drive C:

Settings for consoles purchased before 12/10/94

Maxtor:

Type=47, Cyln=936, Head=16, Wpcom=0, Lzone=936, Sect=17, Size=124MB

Quantum:

127AT: Type=47, Cyln=814, Head=9, Wpcom=0, Lzone=814, Sect=32, Size=114MB

170AT: Type=47, Cyln=968, Head=10, Wpcom=0, Lzone=968, Sect=34, Size=170MB

210AT: Type=47, Cyln=873, Head=13, Wpcom=0, Lzone=873, Sect=36, Size=210MB

Settings for consoles purchased after 12/10/94

Quantum:

****127AT: Type=47, Cyln=919, Head=16, Wpcom=0, Lzone=919, Sect=17, Size=127MB

170AT: Type=47, Cyln=1011, Head=15, Wpcom=0, Lzone=1011, Sect=22, Size=170MB

****210AT: Type=47, Cyln=723, Head=15, Wpcom=0, Lzone=723, Sect=38, Size=210MB

240AT: Type=47, Cyln=723, Head=13, Wpcom=0, Lzone=723, Sect=51, Size=240MB

270AT: Type=47, Cyln=944, Head=14, Wpcom=0, Lzone=944, Sect=40, Size=270MB

340AT: Type=47, Cyln=1011, Head=15, Wpcom=0, Lzone=1011, Sect=44, Size=340MB

****365AT: Type=47, Cyln=976, Head=12, Wpcom=0, Lzone=976, Sect=61, Size=365MB

420AT: Type=47, Cyln=1010, Head=16, Wpcom=0, Lzone=1010, Sect=51, Size=420MB

425AT: Type=47, Cyln=1021, Head=16, Wpcom=0, Lzone=1021, Sect=51, Size=425MB

525AT: Type=47, Cyln=1017, Head=16, Wpcom=0, Lzone=1017, Sect=63, Size=525MB

When Looking at Quatum Hard Drive, the settings are listed as follows:

C/H/S: 127=919/16/17; 170=1011/15/22; for example

C=Cylinders

H=Heads

S=Sectors

Other manufacturers may list the settings in a similar way.

Note: The Lzone is always the same as the Cyln= number.

****Popular versions of Hard Drives used in Obsession Consoles.

ALL NEW DRIVES USE 1017 16 0 1017 63

1280AT : 2484 , 16 , 0 , 2484 , 63

840AT : 1647 , 16 , 0 , 1647 , 63